

In the Specification:

Replace Page 1, lines 17-19 as follows:

United States Patent Application No. 09/846,886, entitled **OPTICAL**

LIMITER BASED ON NONLINEAR REFRACTION, filed on May 1, 2001 in

the names of Edward H. Sargent and Luasz Brzozowski.

Replace Page 10, lines 20-24 as follows:

B₂ ~~When the input signal X2 is one (high) and the input signal Y2 is zero~~
(low), the combined input to the limiter is I1. The reflected signal output of
the limiter is I1 when the input to the limiter is I1. The gain element outputs a
one (high) when its input is I1. Therefore, the output of the all-optical XOR
gate is one (high).

Replace Page 11, lines 1-3 as follows:

B3 the limiter is zero (low) when the input to the limiter is I2. The gain element
outputs a zero (low) when its input is zero (low). Therefore, the output of the
all-optical XOR gate is zero (low).
